

E-FILED ON 13 DECEMBER 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of inventor(s):

Albert Sun et al.

Application No. **10/699,764**

Filing Date: **03 November 2003**

Title: **In-Circuit Configuration
Architecture for Embedded
Configurable Logic Array**

Confirmation No. **4234**

Group Art Unit: 2186

Examiner: Hetul B. Patel

CUSTOMER NO. 22470

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUPPLEMENTAL RESPONSE

Sir:

In further response to the non-final Office Action mailed 24 August 2006, Applicants submit the attached terminal disclaimers and fees, which did not accompany the substantive response filed 8 December 2006.

It is respectfully submitted that this application is now in condition for allowance, and such action is requested.

The Commissioner is hereby authorized to charge any fee determined to be due in connection with this communication, or credit any overpayment, to our Deposit Account No. 50-0869 (MXIC 1520-1).

Respectfully submitted,



Mark A. Haynes, Reg. No. 30,846

Dated: 13 December 2006

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